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202.828.3455 DIRECT
202.857.6395 FAX
orum.george@arentfox.com

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U.S. Patent Appln. S.N. 09/359,767

For: SEMICONDUCTOR MEMORY DEVICE AND
METHOD FOR EXECUTING SHIFT REDUNDANCY
OPERATION

By: Satoshi ETO et al

Our Ref: 100021-09026

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NO. 5314 P. 2

PTO/SB/122 (04-05)

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Application Number	09/359,767
Filing Date	July 22, 1999
First Named Inventor	Satoshi ETO et al
Group Art Unit	2751
Examiner Name	
Attorney Docket Number	100021-09026

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